

Annexure - III

Bidder eligibility Criteria

This form has to be filled, signed & submitted by the bidder along with the technical bid.

Sr.	Eligibility Criteria	Eligible Yes /No
1.	The bidder should be an OEM. If an OEM is unable to quote directly then the bid may be made through a single, qualified System Integrator (SI) with appropriate justification. In the latter case a letter of authorisation from the OEM, specific to this tender, must be enclosed with the bid.	
2.	One OEM can authorize only one SI for the execution of this project.	
3.	One SI can represent only one OEM for the execution of this project.	
4..	The OEM will be responsible for Supply, installation, configuration, commissioning, testing, maintenance and support for both hardware and software during the warranty period. The OEM should submit an authorisation letter to IUCAA on the OEM's letterhead stating that "All the equipments and components supplied will have 5 (Five) years onsite comprehensive warranty with 24x7 support along with 4 hours response time and resolution time of Next Business Day (NBD), covering all parts & labor starting from the date after the successful installation, demonstration of performances and acceptance by IUCAA. The defects, if any, during the warranty period will be rectified free of charge by arranging free replacement wherever necessary. It will be completed within Next Business Day resolution for entire solutions after the intimation of fault."	
5..	The installation/ integration should be done by OEM engineers only. OEM should submit engineers details such as engineer name, employee code, total years of experience in HPC, and number of HPC installations carried out by that particular engineer on their letter head. A copy of installation report must be enclosed with the technical bid.	
6.	The OEM should submit documentary evidence of purchase order (PO) for the successful installation of distributed memory node cluster, with only CPUs, of at least 100TF (or higher) peak performance, in India at government organizations/research institutes in a single order. Please submit copy of 3 such orders in the last 5 years i.e. starting from 1st January, 2014 onwards, with duly signed installation report with details of OEM engineer.	
7.	If the bidder happens to be a SI, the bidder should submit documentary evidence of purchase order (PO) for successful installation of distributed memory node cluster, with only CPUs, of at least 75 TF peak performance, in India at government organizations/research institutes in a single order. Please submit a copy of 3 such orders in the last 3 years i.e. from 1st January, 2016 onwards, with duly signed installation report with details of OEM engineer.	
8.	Both the OEM and the SI should have a local office in Pune. They must share the office address along with the bid.	
9.	The OEM and SI should be in the HPC business for the last 5 years in India. Please submit documentary evidence for the same.	
10.	The OEM should have a minimum of 10 entries in the worldwide Top 500 Supercomputer list consistently in each list published in last three years i.e. June-November 2016, June-	

	November 2017, June-November 2018.	
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