Annexure-I

General Instructions for Bidders:

1. The entire solution should be based on certified hardware/software components, which are fully validated and supported by the OEM.
2. Vendor must quote for all the requirements together viz HPC cluster, shared storage and Interconnect for the proposed solution. Partial response to the tender will be rejected.
3. HPC cluster solution must have dense computing platform based compute nodes.
4. All equipment's must be compatible with existing racks, PDUs and Indian electrical standards. Engineering documentation (in digital form ONLY) on the physical sizes, weights of all major components must be submitted along with the technical bid.
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6. The server, chassis management and monitoring software must be from the OEM itself.
7. The proposal must include 1) detailed data sheets (in digital form ONLY) for every single proposed component & the necessary technical white papers discussing features, performance and optimized technique 2) pictorial representation of the entire solution indicating required rack size in U and 3) the power and cooling requirement with supporting documents 4) Complete Bill of Materiel (BOM).
8. The warranty and support of the total solution shall commence from the date of final acceptance test and sign off on an acceptance report.
9. The entire solution should fit within 30 U sized rack and its total power requirement should not exceed 15 KW. The solution must be implemented within the already existing racks, PDUs available at the IUCAA data centre. The bidder may inspect IUCAA facilities before quoting.

OEM/Bidder qualifying criteria:

1. Only those bidders fulfilling the following criteria should respond to the tender and submit documentary evidence for each of the following, failing to which bid is liable to be rejection.
2. The bidder should be an OEM/Authorized system integrator (SI) of the OEM and a Letter of Authorization from the OEM, specific to this tender should be enclosed by the bidder.
3. One OEM can authorize only one SI for the execution of this project.
4. One SI can represent only one OEM for the execution of this project.
5. The OEM will be responsible for supply, installation, configuration, commissioning, testing, maintenance and support for both hardware and software during the warranty period. The OEM should submit an authorisation letter **to IUCAA** on the OEM's letterhead stating that "The OEM will supply, configure, install, test, commission, maintain and provide 24 X 7 on-site support for the complete Hardware and Software solution during the full 6 years of warranty period with 4 hours of response time and 24 hours of resolution time for an hardware related issue/problem and 72 hours of resolution time for any other issues"
6. The installation will be done by OEM engineer only. OEM should submit engineer details such as engineer name, letter from HR mentioning employee id and experience in HPC, number of HPC installations carried out by engineer and customer satisfaction report along with the bid.
7. OEM should have installed a distributed memory node cluster, with only CPUs, of at least 50TF (or higher) peak performance, in India at government organizations/research institutes in a single order. Please submit 3 such orders in last 3 years i.e., from 1st March 2019 onwards.
8. SI should have installed a distributed memory node cluster, with only CPUs, of at least 30 TF peak performance, in India at government organizations/research institutes in a single order. Please submit 2 such orders in the last 3 years i.e., from 1st March 2019 onwards.

9. The OEM should have own office and service centre in Pune.
10. The SI should have own office and service centre in Pune.
11. The OEM and SI should be in the HPC business for the last 10 years in India.
12. The bidder should have a minimum of 3 entries in the India's top supercomputer latest list (http://topsc.cdacb.in/topsc.php).
13. The OEM should have a minimum of 10 entries in the worldwide Top 500 Supercomputer consistently in each list published in last three years.
14. The bidder should carry out all the benchmarks as mentioned in "Technical Benchmark" section and submit the results (in digital form) along with the bid.
15. If the performance (average performance of mentioned benchmarks) of the solution deviates more than 10% of the best performance (average performance of mentioned benchmarks) then the solution is liable for rejection.